

3EC-M2S-VLP

- 极低粗糙度铜箔，适用于细线路蚀刻之减成法制程。
Low profile copper foil, suitable for fine etching in the subtractive process.
- 高抗张力，于薄铜箔易于操作。
High tensile strength, easy to handle thin foil.
- 最薄之7 μ m铜箔具量产能力。
From 7 μ m, we line up.

用途/Application

- IC封装基板
/Semiconductor Package

构成/Composition



生产地点/Production Site

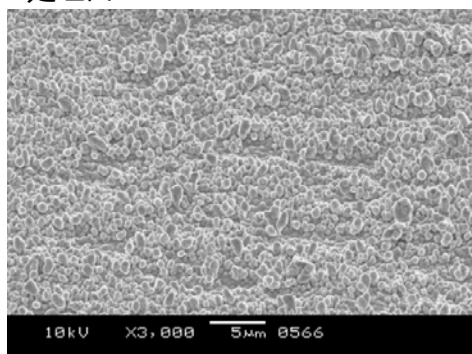
- 日本 / Japan

代表性特性数据/Representative data

	μ m	Area weight (g/m ²)	Laminate side Rz(μ m)	Tensile Strength (N/mm ²)	Elongation (%)	Peel Strength (kg/cm)@FR-4
3EC-M2S-VLP	9	80	1.8	500	4	0.6
	12	107	1.8	500	5	0.7
	18	153	1.8	500	8	0.8

※上述表列为代表性数据非保证值。
This is representative data, not guaranteed.

处理面/Laminate side



阻剂面/ resist side

